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10/06/05  
06/05/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

21125

APPL NUM 10064051	FILING DATE 06/05/2002	CLASS 257	SUBCLASS 592	GAU 2811	EXAMINER 285 <i>Edith Lee</i> LANDAN
**APPLICANTS: Chen Anchor;					
**CONTINUING DATA VERIFIED:					
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** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO NAUP0486USA	
TITLE : Bipolar junction transistor and fabricating method					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.    Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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